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Attorney Docket No. 01213/TL**IN THE UNITED STATES PATENT  
AND TRADEMARK OFFICE**

Applicant(s): R. LEYDIER et al.

Serial No. : 09/807,686

Filed : April 16, 2001

For : INTEGRATED CIRCUIT CHIP MADE  
SECURE AGAINST THE ACTION OF  
ELECTROMAGNETIC RADIATIONArt Unit :  
Examiner :**CERTIFICATE OF MAILING**

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231, on the date noted below.

Attorney: Thomas LangerDated: June 25, 2001

In the event that this Paper is late filed, and the necessary petition for extension of time is not filed concurrently herewith, please consider this as a Petition for the requisite extension of time, and to the extent not tendered by check attached hereto, authorization to charge the extension fee, or any other fee required in connection with this Paper, to Account No. 06-1378.

**LETTER TO OFFICIAL DRAFTSPERSON  
SUBMISSION OF FORMAL DRAWINGS**Assistant Commissioner for Patents  
Washington, D.C. 20231

S I R :

Submitted herewith are five (5) sheets of formal drawings (Figs. 1-7D) which should replace the corresponding sheets presently on file.

Figs. 3A, 3B, 4A, 4C, 6A, 6C, 6D, 7D submitted herewith incorporate the changes made in a proposed drawing correction filed on April 16, 2001.

Approval and entry are respectfully requested.

Respectfully submitted,

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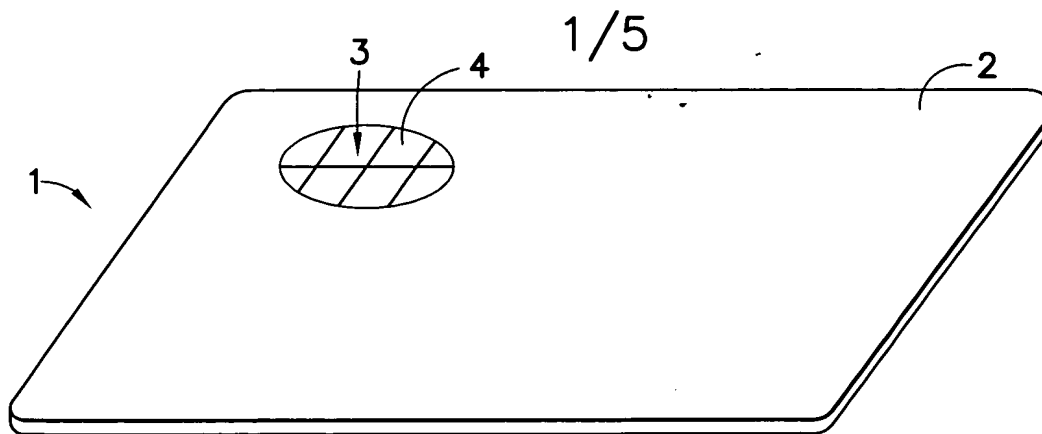


FIG. 1

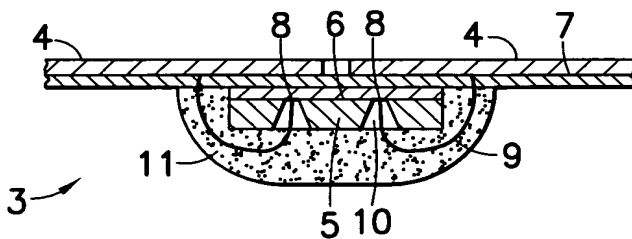


FIG. 2

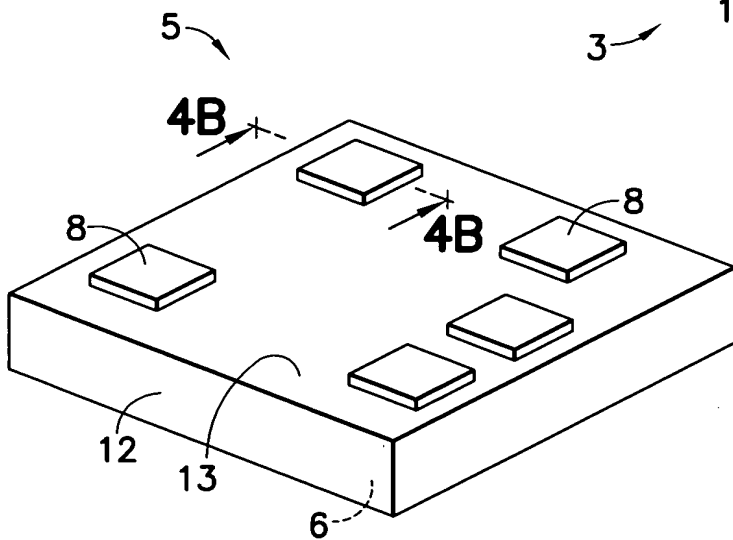


FIG. 3A

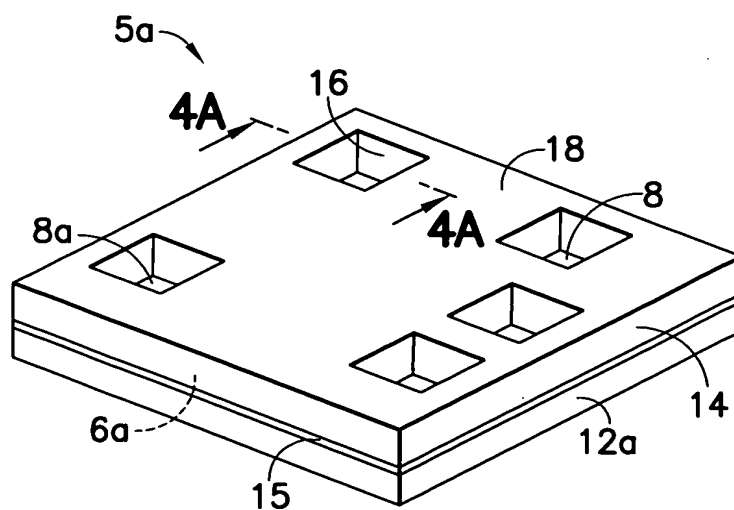


FIG. 3B

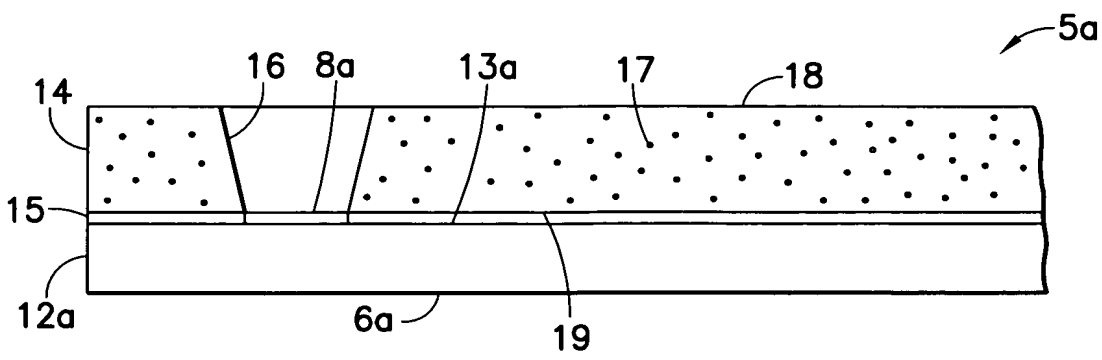


FIG.4A

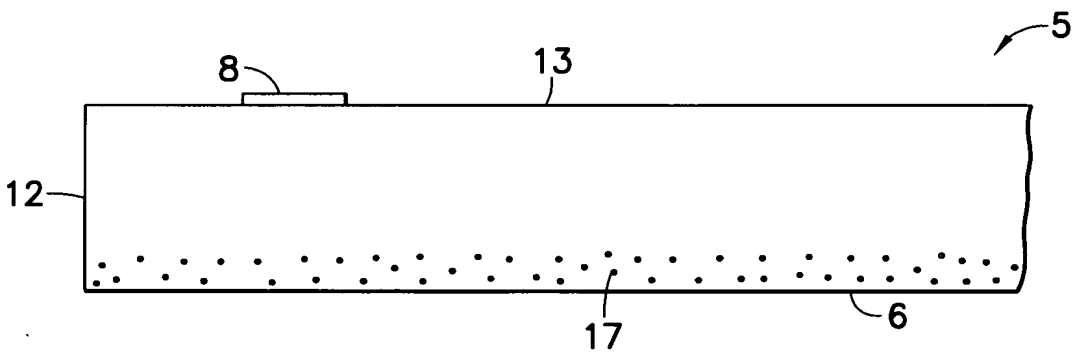


FIG.4B

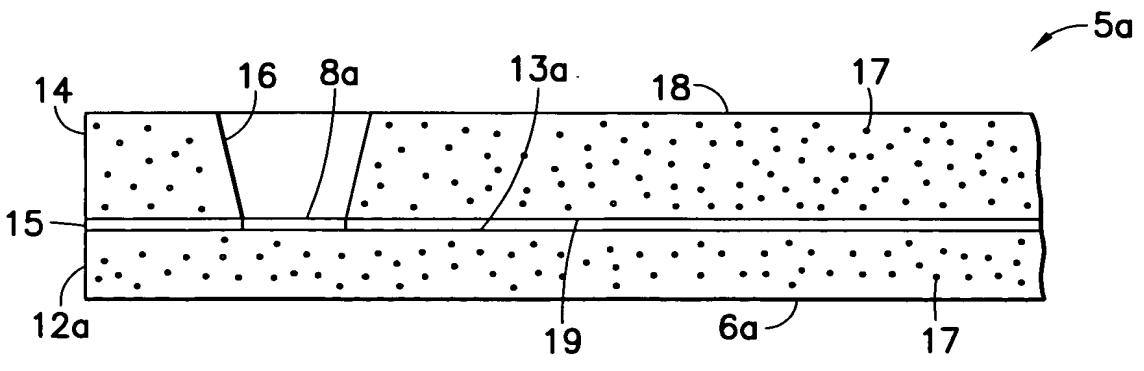


FIG.4C

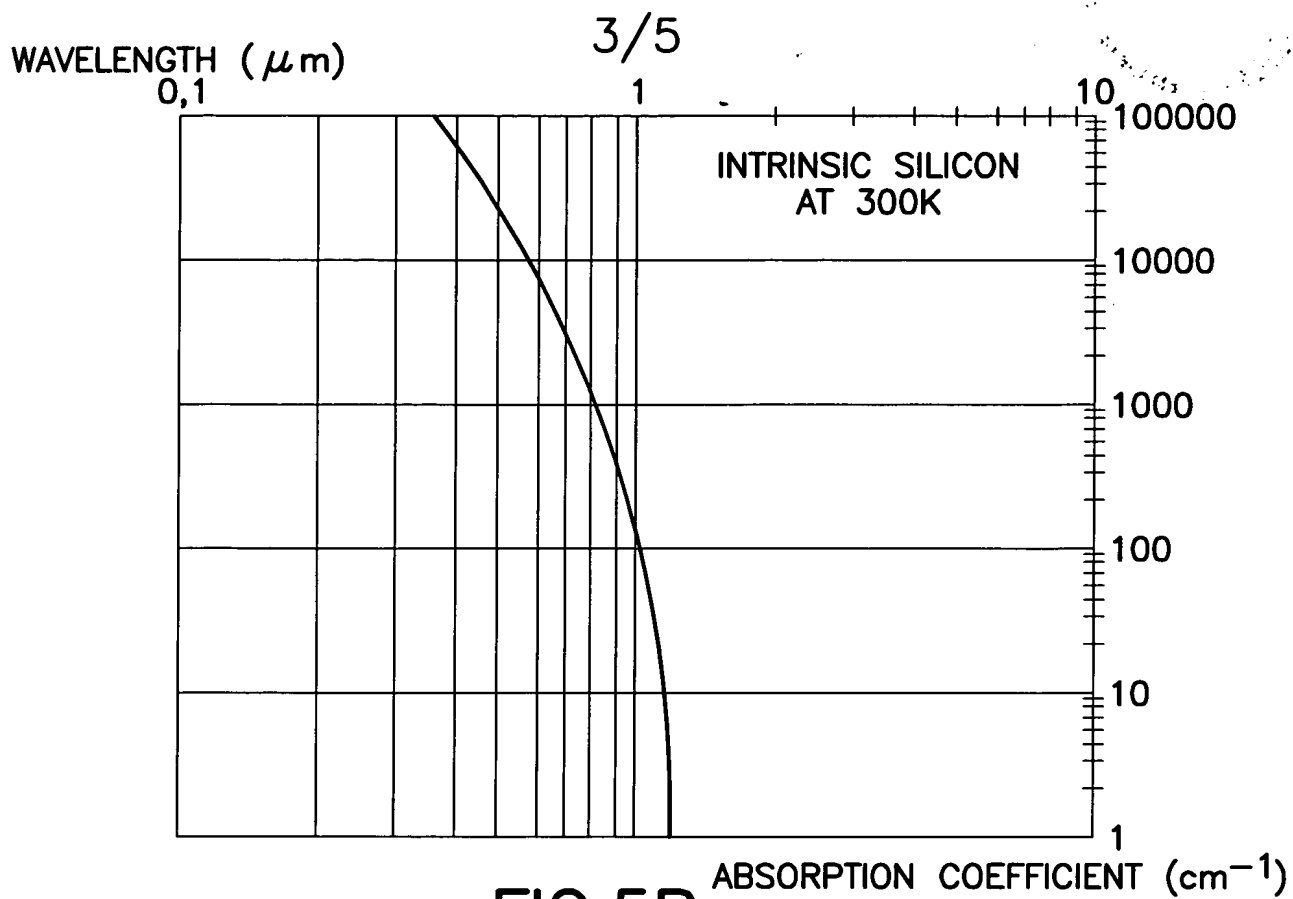


FIG.5B

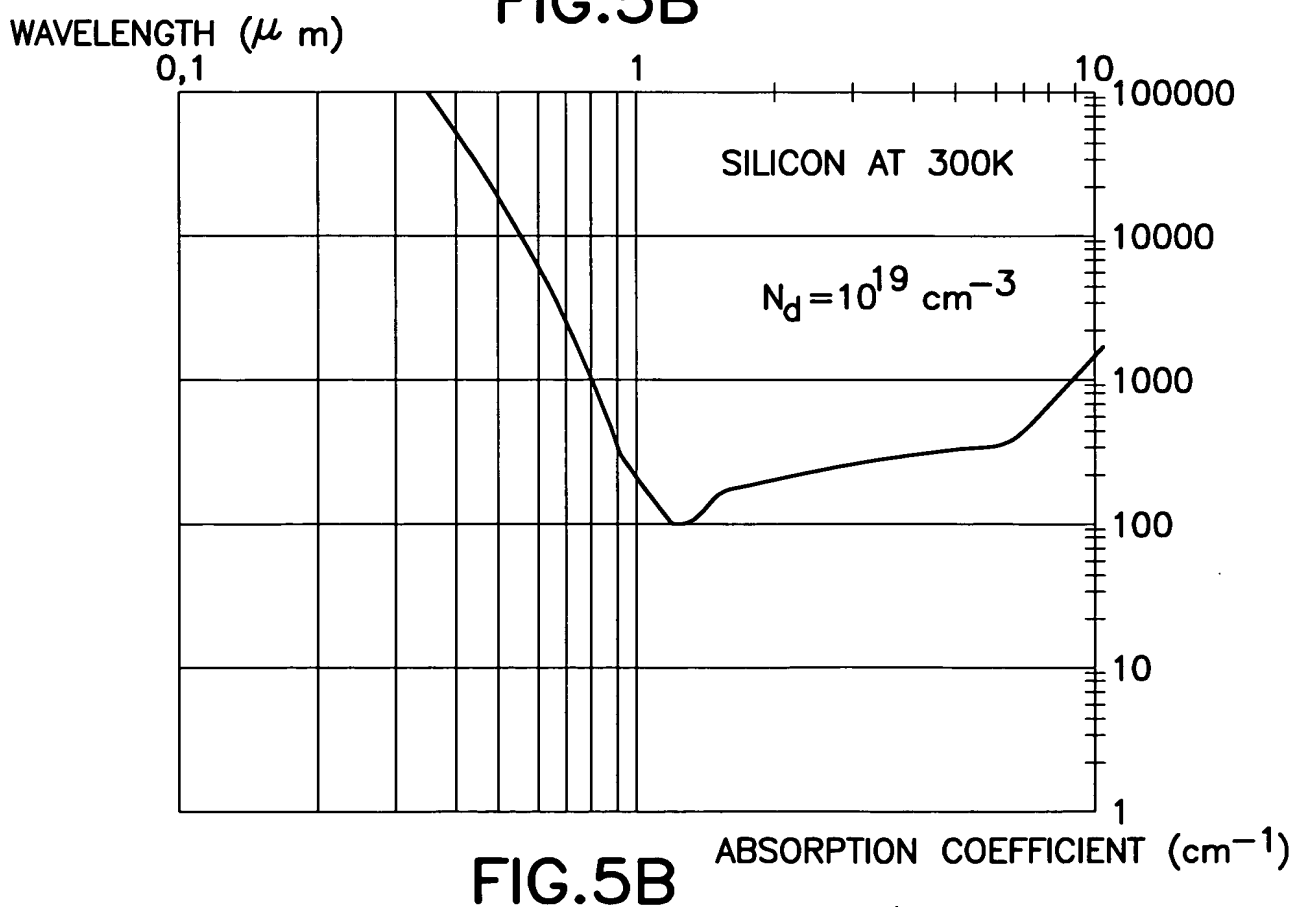
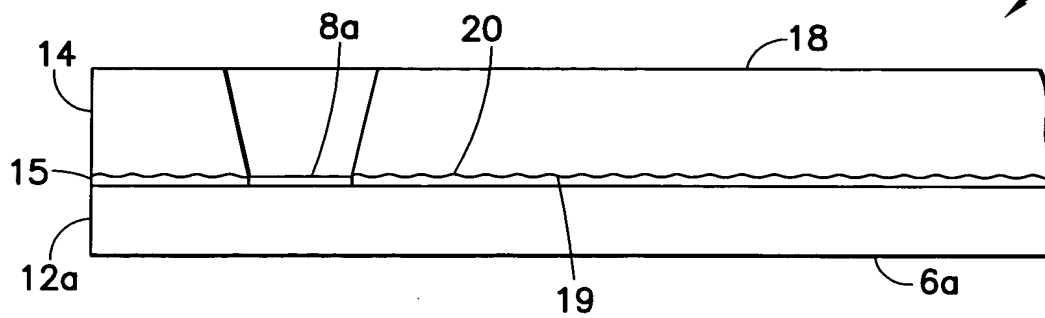


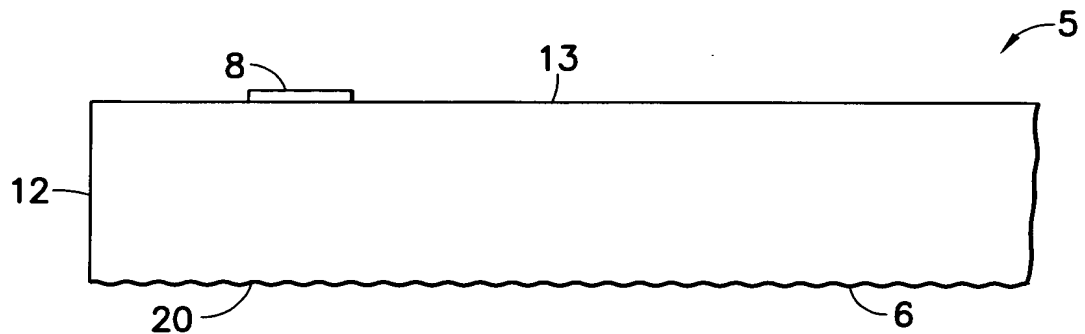
FIG.5B

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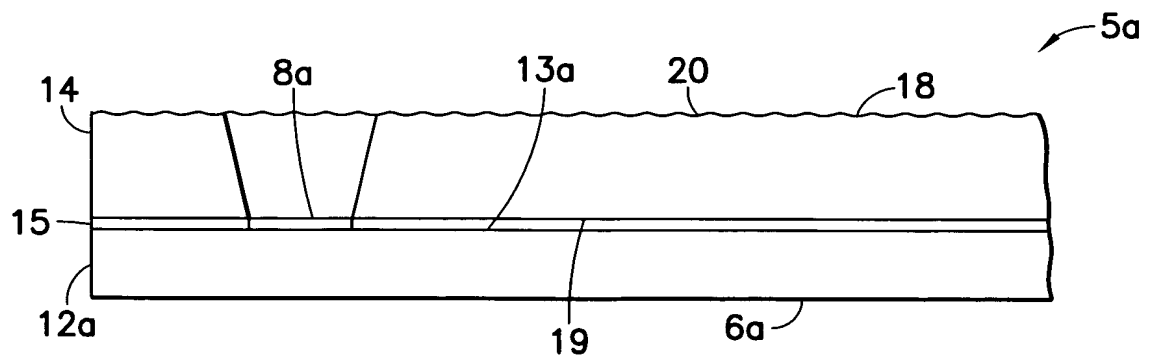
**5a**



**FIG. 6A**



**FIG. 6B**



**FIG. 6C**

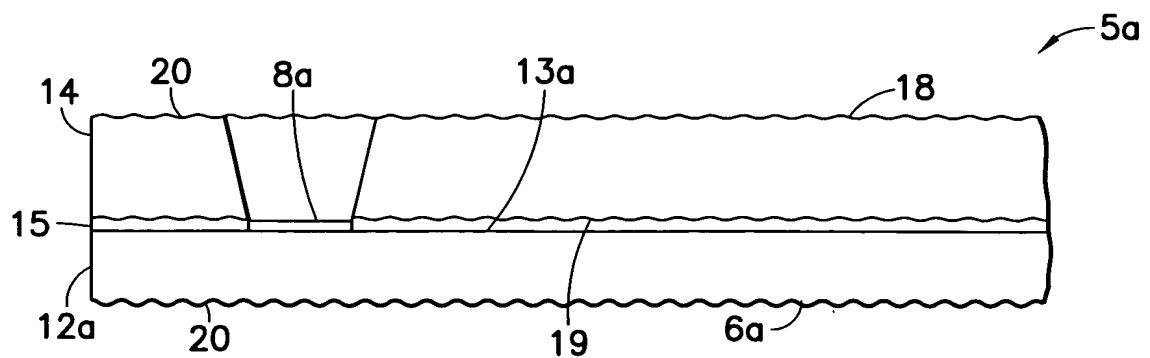


FIG. 6D

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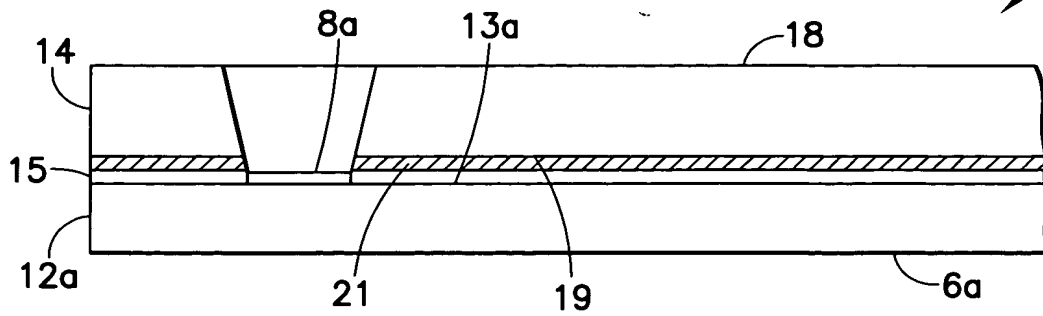


FIG. 7A

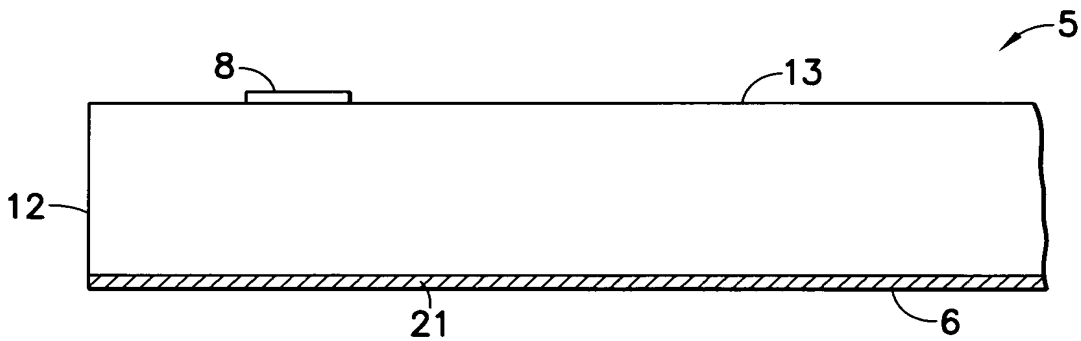


FIG. 7B

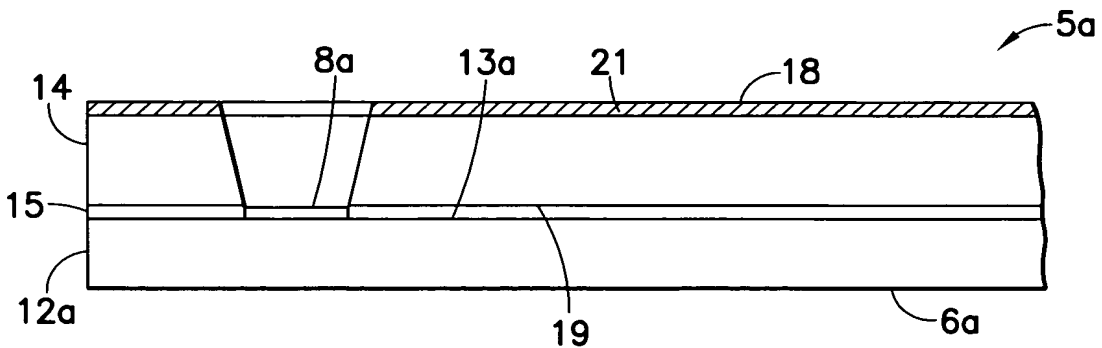


FIG. 7C

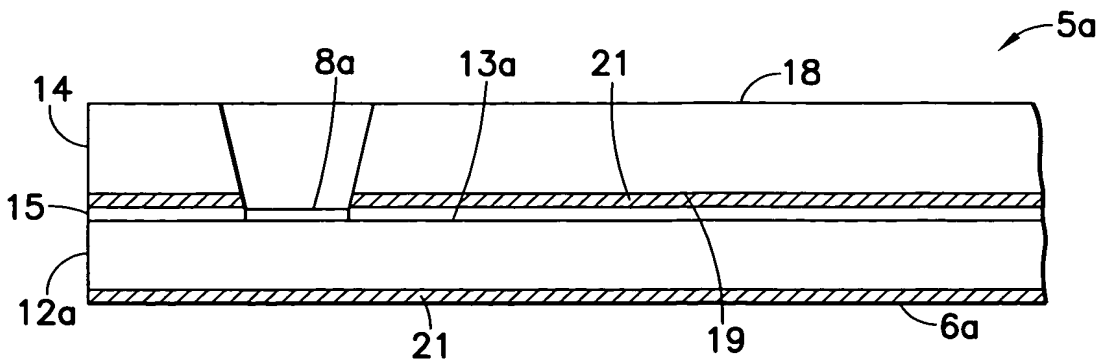


FIG. 7D